MU3D

SURFACE MOUNT
ULTRAFAST RECTIFIER
VOLTAGE：200V CURRENT： 3.0A

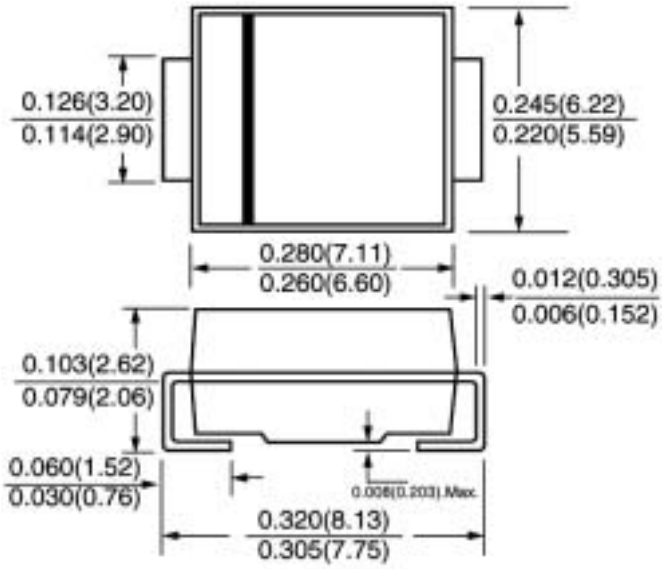
FEATURE

Plastic package has Underwriters Laboratories Flammability Classification 94V-
Ideally suited for use in very high frequency switching power supplies, inverters and as free wheeling diodes
Ultrafast recovery time for high efficiency
High surge capability
High temperature soldering guaranteed
260°C/10sec/at terminals
Glass passivated chip

MECHANICAL DATA

Terminals: Solder plated, solderable per MIL-STD-750, Method 2026
Case:JEDEC DO-214AB molded plastic body over passivated chip
Polarity:Color band denotes cathode end
Weight: 0.007 ounce, 0.21 gram

SMC／DO—214AB



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(single-phase, half-wave, 60HZ, resistive or inductive load rating at 25℃, unless otherwise stated, for capacitive load, derate current by 20%)

	SYMBOL	MU3D	units
Maximum Recurrent Peak Reverse Voltage	Vrrm	200	V
Maximum RMS Voltage	Vrms	140	V
Maximum DC blocking Voltage	Vdc	200	V
Maximum Average Forward Rectified Current 3/8 ″ lead length at : T_L =140℃ T_L =130℃	If(av)	3.0 4.0	A
Peak Forward Surge Current 8.3ms single half sine- wave superimposed on rated load	Ifsm	125.0	A
Maximum Instantaneous Forward Voltage at rated forward current T_J =25℃ If =3.0A	Vf	0.875	V
Maximum DC Reverse Current Ta =25℃ at rated DC blocking voltage Ta =150℃	Ir	5.0 150.0	μA μA
Maximum Reverse Recovery Time (Note1)	Trr	25	nS
Typical Junction Capacitance (Note 2)	Cj	80	pF
Typical Thermal Resistance, junction to lead	R(jl)	11	℃/W
Storage and Operating Junction Temperature	Tstg, Tj	-50 to +175	℃

Note：
1. Reverse Recovery Condition If =0.5A, Ir =1.0A, Irr =0.25A
2. Measured at 1.0 MHz and applied reverse voltage of 4.0Vdc

RATINGS AND CHARACTERISTIC CURVES MU3D

[1]

Fig. 1 – Forward Current Derating Curve

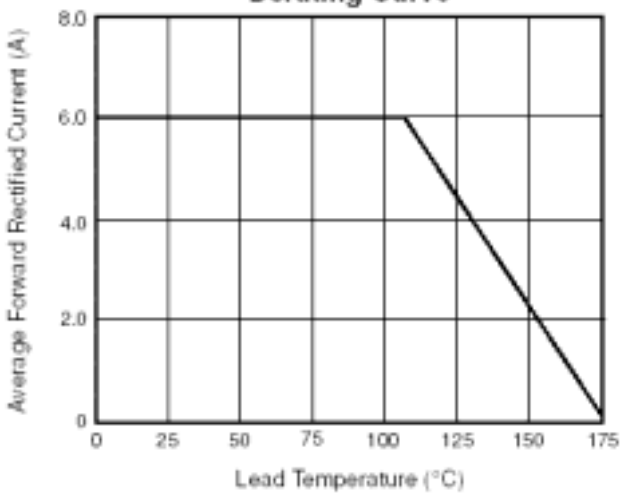


Fig. 2 – Maximum Non-Repetitive Peak Forward Surge Current

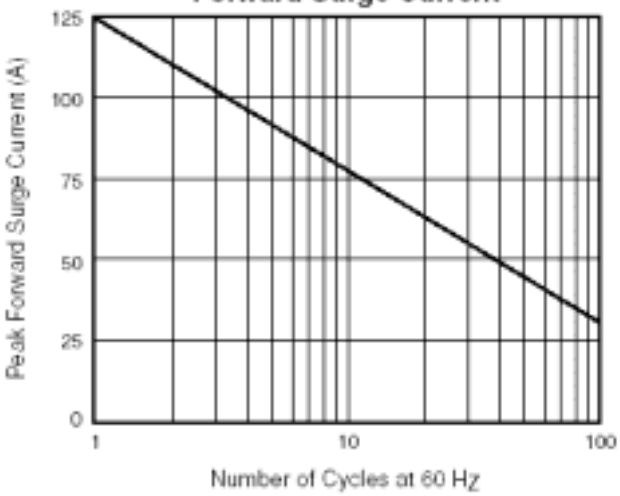


Fig. 3 – Typical Forward Voltage

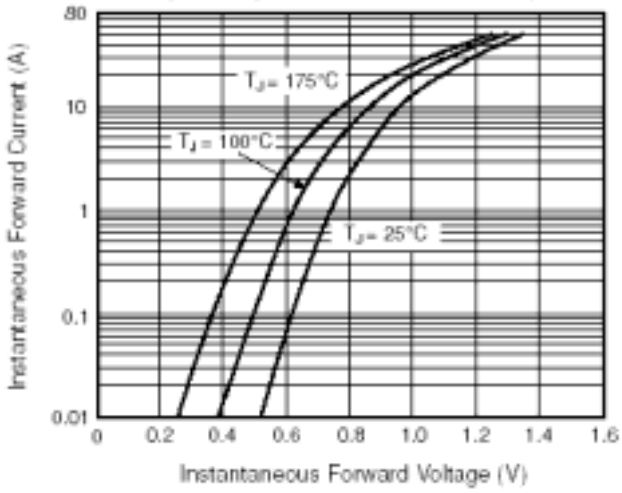


Fig. 4 – Typical Reverse Leakage Characteristics

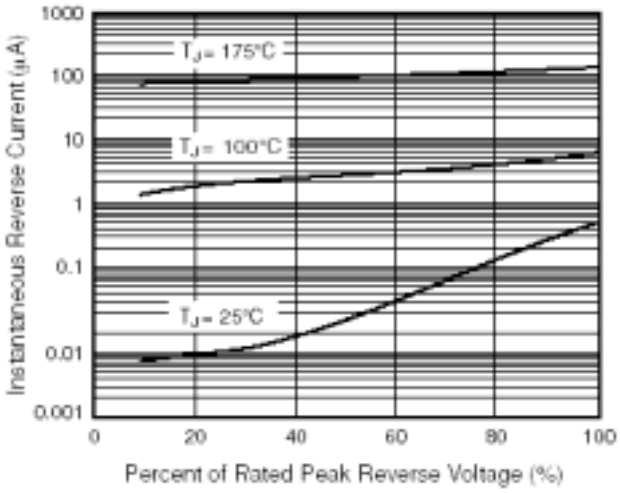
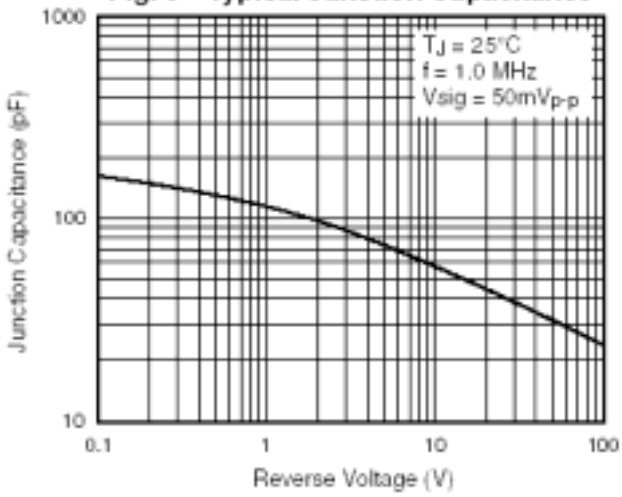


Fig. 5 – Typical Junction Capacitance



[1] Rev.A1, Dec-25-04